

HIGH-FREQUENCY PHASE/FREQUENCY DETECTOR
WITH IMPROVED RESET MECHANISM

ABSTRACT

5 The present invention provides a method and an apparatus
for generating a phase error signal from a reference signal
and a feedback signal using a modified reset generation
mechanism. An input circuit receives a reference signal and
a feedback signal. A phase error detector circuit generates a
10 phase error signal based on the reference signal and feedback
signal. The input circuit is reset and, after a delay, the
phase error detector circuit is reset. The delay is selected
so that there is no jitter associated with the dead zone.